

● Wiring diagram (WIREFMAP) test function:

After entering the wiring diagram (WIREFMAP) test function, the tester shall carry out wiring diagram (WIREFMAP) test and displays as follows while checking is being undertaken:

```
-----TESTING-----  
12345678...
```

Test Result 1: Short circuit (SHORT)

It displays as follows if there is any short circuit in cable or terminal: (e.g. 12 short circuit in the sample)

```
SHORT:  
12
```

At the moment, push **▲▼** key to restart testing or push **PAIR&L** key to return main menu.

Always correct short circuit error first and then start further measurement.

Test Result 2: Neither far-end matcher (ID) is found nor cable inserts local port (L)

The tester will automatically detect far-end matcher (ID) or local port (L) cable and it will display as follows if the far-end of cable to be checked does not insert into the far-end matcher (ID) or if the cable does not insert into the local port (L) in local test:

```
NO ADAPTER:
```

At the moment, push **▲▼** key to restart testing or push **PAIR&L** key to return main menu.

Test Result 3: Normal wiring diagram (WIREFMAP) display

The tester will automatically detect far-end matcher (ID) or local port (L) cable and it will display wiring diagram (WIREFMAP) as follows if it is found the far-end matcher (ID) or the local port (L) on the far-end of cable to be checked:

```
WIREFMAP: PASS  
R: 12345678 ID1  
|||||||  
M: 12345678
```

R line shows RJ45 jack pin number of far-end port and "ID1" is the far-end matcher number.

I line shows the connecting line between far-end port and master port.

M line shows the RJ45 jack pin number of master port.

At the moment, push **▲▼** key to restart testing or push **PAIR&L** key to return main menu.

Test Result 4: Wiring diagram (WIREFMAP) display when there is an open circuit at the far-end of cable.

```
WIREFMAP: FAIL  
R: 12x45x78 ID1  
|||||||  
M: 12345678
```

R line "3" and "6" pins location display "x", it indicates an open circuit in far-end plug "3" and "6" pins and the open circuit is located nearby the far-end plug. (The open circuit should be located within 10% cable length if it is measured from the far-end plug)

Note: If you use far-end matcher (ID) to measure wiring diagram (WIREFMAP), because the test is made via the paired two cable cores, the open circuit at the far-end always displays in pair as shown above where there is one open circuit or all are open circuits in the far-end "3" and "6" pins. For identification, it is simple to move the tester to the far-end to have the measurement.